

What is claimed is:

1. A system for keeping the scores of single and multiple players playing the various games of darts, said system comprising:

a case,

said case having a front face,

said front face having two orthogonal arrays of display windows formed therein for displaying representative standard dart game scores, a plurality of number keys and cancel buttons equal in number to the number of said display windows, two keypads, each keypad having a four digit display,

electronic circuitry means for connecting and controlling said display windows, said number keys and said keypads,

said circuitry means being programmed for activating and scoring a plurality of dart score games, providing control over input errors, allowing scorekeepers to recall previous scores, and making other corrections in the score displays.

2. A system for keeping the scores of single and multiple players playing the various games of darts according to claim 1, wherein;

said keypad comprises a four column by five row cross point matrix configuration for generating a numeric value/function command,

said electronic circuitry means having a microprocessor,

said microprocessor comprises processing firmware, a read only memory, programmed to contain the instructions in the source code list, and a microprocessor data/command input-output generator and computational device, and

each of said display windows comprises a seven segment display using light emitting diode arrays which are separate elements arranged in a straight line figure eight for displaying the desired numeric values 0 - 9.

3. A system for keeping the scores of single and multiple players playing the various games of darts, said system comprising:

a case,

said case having a front face,

said front face having;

two orthogonal arrays of display windows formed therein for displaying representative standard dart game scores, each of said display windows comprising a seven segment display using light emitting diode arrays which are separate elements arranged in a straight line figure eight for displaying the desired numeric value 0 - 9,

a plurality of number keys and cancel buttons equal in number to the number of said display windows,

two keypads, each keypad having a four by five row cross point matrix configuration for generating a numeric value/function command,

electronic circuitry means for connecting and controlling said display windows, said number keys and said keypads, said electronic circuitry means having a microprocessor, and

said microprocessor having processing firmware, a read only memory, programmed to contain the instructions in the source code list, and a microprocessor input-output generator and computational device.